

WEST Search History

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DATE: Friday, January 13, 2006

| <u>Hide?</u> | <u>Set Name</u> | <u>Query</u> | <u>Hit Count</u> |
|--|-----------------|--|------------------|
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <input type="checkbox"/> | L14 | L13 And PSoC Design | 0 |
| <input type="checkbox"/> | L13 | I2 and programmable block | 54 |
| <input type="checkbox"/> | L12 | I7 and programmable block | 1 |
| <input type="checkbox"/> | L11 | L7 and L1 | 2 |
| <i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <input type="checkbox"/> | L10 | L4 | 7 |
| <input type="checkbox"/> | L9 | L3 | 340 |
| <input type="checkbox"/> | L8 | L7 | 0 |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <input type="checkbox"/> | L7 | L6 and programmable | 557 |
| <input type="checkbox"/> | L6 | L5 and assembly | 742 |
| <input type="checkbox"/> | L5 | L4 and counter | 1735 |
| <input type="checkbox"/> | L4 | L3 and (analog-to-digital or ADC) | 2903 |
| <input type="checkbox"/> | L3 | L2 and timer | 12980 |
| <input type="checkbox"/> | L2 | micro?controller or microcontroller | 66663 |
| <input type="checkbox"/> | L1 | (717/106 717/107 717/108 717/109 717/110 717/111 717/112 717/113).ccls. | 1492 |

END OF SEARCH HISTORY